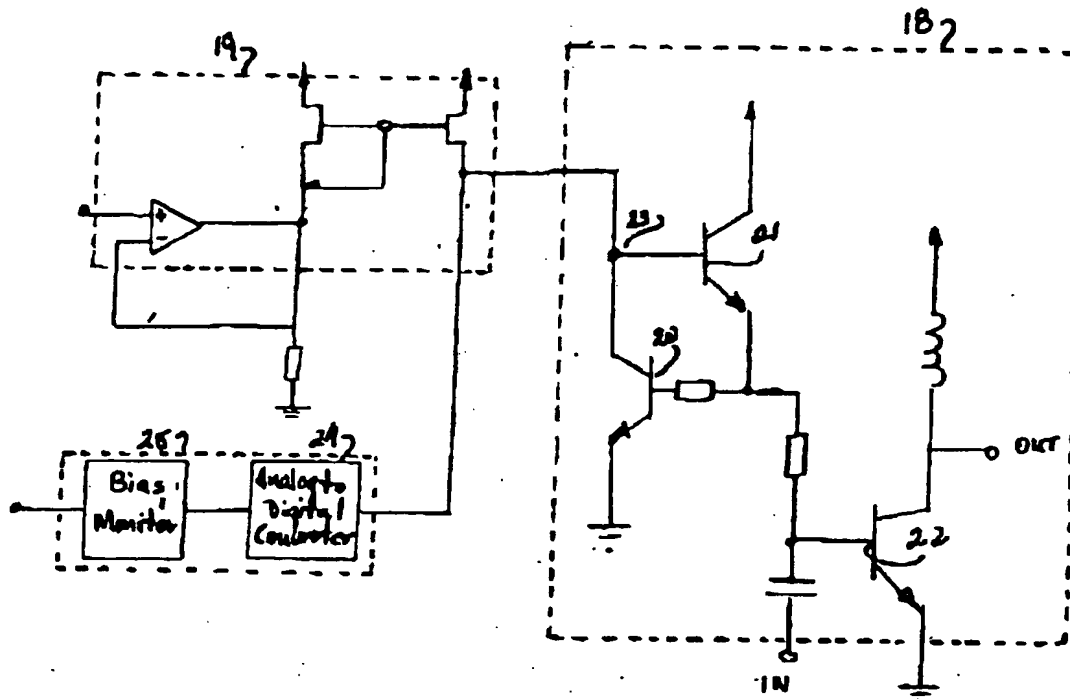


The diagram illustrates a system architecture for a transmitter and receiver. A dashed box labeled 127 encloses the transmitter and receiver components. The transmitter (167) includes a MODULATOR (157) and a Power Amplifier (187). The receiver (137) includes a DEMODULATOR (147) and a Front End (287). An ANTENNA (117) is connected to both the transmitter and receiver. A MAIN CONTROL PROCESSOR (177) is connected to the transmitter, receiver, and a BIAS CONTROL (197) block. A MEMORY (277) block is connected to the MAIN CONTROL PROCESSOR. A TEMPERATURE CONTROL ALGORITHM (267) block is connected to the MAIN CONTROL PROCESSOR. A BIAS / MONITOR (257) block is connected to the BIAS CONTROL and the MAIN CONTROL PROCESSOR. A signal 10 is shown entering the system.

**FIGURE 1**



**FIGURE 2**

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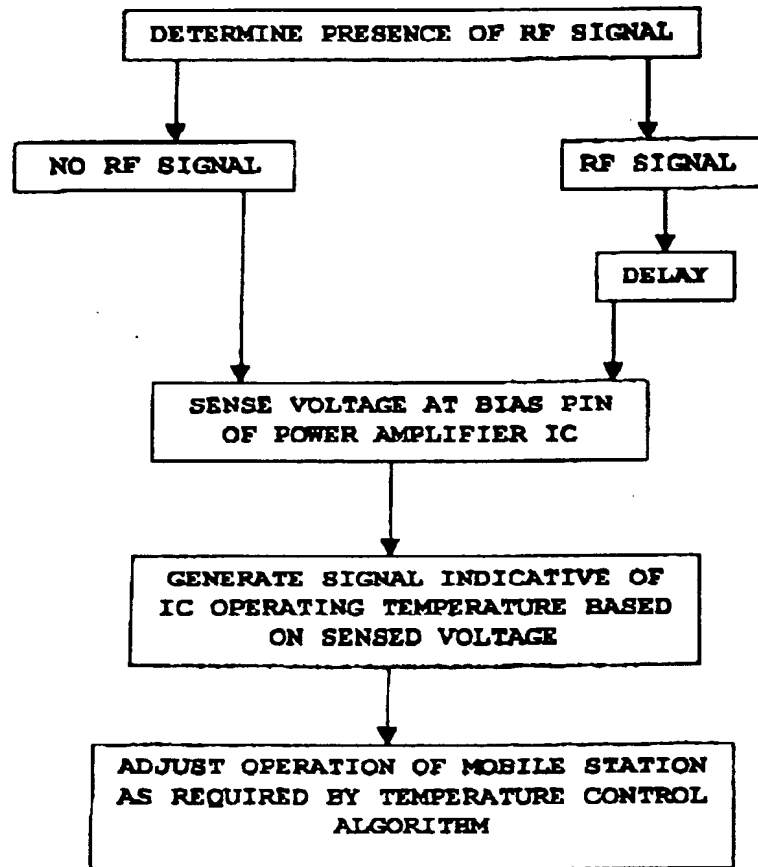


Figure 3